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January 4, 2006TO: Commissioner for
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FROM: Douglas P. Mueller

OUR REF: 10921.261USWO
TELEPHONE: (612) 455.3800

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Title of Document: Request for Corrected Filing Receipt
Change of Address
Filing Receipt with Corrections

Applicant: Torayuki Tsukada
Serial No.: 10/518,224
App. Filed: December 16, 2004
Group Art No.: 2832

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Torayuki Tsukada Serial: 10/518,224
Filed: December 16, 2004 Docket No.: 10921.0261USWO
Title: CHIP RESISTOR HAVING LOW RESISTANCE AND METHOD OF
MAKING THE SAME

CERTIFICATE UNDER 37 CFR 1.6. I hereby certify that these papers are being transmitted via facsimile to the U.S. Patent and Trademark Office on January 4, 2006.

By:

Name: Yuhgo Maruza

REQUEST FOR CORRECTED FILING RECEIPT

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Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a photocopy of the filing receipt from the United States Patent and Trademark Office in the above-identified application showing requested corrections.

The Assignment For Published Patent Application is incorrect.

Please replace [Kyoyo] with Kyoto.

The Foreign Applications data is incorrect

Please replace [17791] with 177971.

Correction of the records of the United States Patent and Trademark Office and issuance of a corrected filing receipt are respectfully solicited.

Respectfully submitted,

HAMRE, SCHUMANN, MUELLER &
LARSON, P.C.
P.O. Box 2902
Minneapolis, Minnesota 55402
(612) 455-3800

Dated: Jan 3, 2006

DPM/ym

By

Douglas P. Mueller
Reg. No. 30,300



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FILE FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/518,224	12/16/2004	2832	900	10921.261USWO	12	4	2

23552
MERCHANT & GOULD PC
P.O. BOX 2903
MINNEAPOLIS, MN 55402-0903

CONFIRMATION NO. 7058

FILING RECEIPT

10/518,224 12/16/2004 2832 900 10921.261USWO 12 4 2
OC000000015149058

SCANNED

Date Mailed: 06/03/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Torayuki Tsukada, Kyoto, JAPAN;

Assignment For Published Patent Application

ROHM CO., LTD, [Kyoyo] JAPAN KyotoPower of Attorney: The patent practitioners associated with Customer Number 23552.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/07457 06/12/2003

Foreign Applications

JAPAN 2002-177970 06/19/2002

JAPAN 2002-[17791]06/19/2002

177971

Projected Publication Date: 09/08/2005

Non-Publication Request: No

Early Publication Request: No

Title

Chip resistor having low resistance and its producing method

Preliminary Class

338

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